



# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#### In re Application of:

Protigal et al.

Serial No.: 09/776,387

Filed: February 2, 2001

For: INTEGRATED CIRCUIT MODULE HAVING ON-CHIP SURGE CAPACITORS

Examiner: Unknown

Group Art Unit: 2815

**Attorney Docket No.:** 2898.2US (88-070.7)

#### CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

November 20, 2001 Date of Deposit

Signature of registered practitioner or other person beving reasonable basis to expect mailing to occur on date of deposit shown pursuant to 37 C.F.R. § 1.8(a)(1)(i)

Joseph A. Walkowski
Typed/printed name of person whose signature is
contained above

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## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

01/14/2002 HDEMESS1 00000117 09776387



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### **DOCUMENTS**

# U.S. Patent Documents

U.S. Patent No.	Issue Date	<u>Inventor</u>
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"IBM's Response to Micron," (22 pages), December 8, 1995.

Applicants offer to supply any explanation or discussion of the documents which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed after the mailing date of a first Office Action on the merits. The attached documents were not found within the last three months, therefore the fee pursuant to 37 C.F.R. § 1.17(p) is enclosed.

Respectfully submitted,

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Date: November 20, 2001

JAW/hlg:dlm

Enclosures: Form

Form PTO-1449

Copy of documents cited

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